

NEWS RELEASE

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Synopsys Announces Support for Actel's New SmartFusion Intelligent Mixed Signal FPGAs

Synplify Pro FPGA Synthesis Tools Provide Enhanced Timing Optimization for Devices Targeting Embedded Design

MOUNTAIN VIEW, Calif., April 21, 2010 -- Synopsys, Inc. (Nasdaq:SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing today announced enhanced FPGA synthesis support is available for Actel Corporation's (NASDAQ: ACTL) new SmartFusion™ intelligent mixed signal FPGAs. Synopsys' Synplify Pro® FPGA synthesis tools have been enhanced to offer advanced support and timing optimization for the flash-based FPGA architecture that provides the programmable digital portion of the SmartFusion devices.

“Providing programmable digital and analog logic with an embedded processor all in one device is a powerful combination,” said Jeff Garrison, director of FPGA product marketing at Synopsys. “The SmartFusion devices, combined with Synopsys' technology-leading Synplify Pro FPGA synthesis product, provide designers with a robust, cost-saving solution for their embedded applications. Synopsys and Actel have worked in close collaboration for many years for the benefit of our mutual customers and support for SmartFusion devices is the latest example.”

“SmartFusion is a unique solution, integrating a wide variety of functionality into a single chip,” said Jim Davis, vice president, software and systems engineering at Actel. Synopsys continues to support Actel’s existing and emerging products with leading edge FPGA synthesis technology, which along with the Libero Tool Suite provides a complete and valuable design solution to our customers.”

Pricing and Availability

The enhanced version of Synopsys’ Synplify Pro FPGA synthesis tool is available now. Active customers who have obtained Synplify Pro software through the Actel Libero® Integrated Develop Environment or directly from Synopsys will receive support for SmartFusion devices at no extra cost. Please contact your local Actel or Synopsys representative for more information.

About Synopsys FPGA Synthesis Tools

The Synplify Pro software produces high-performing, cost-effective FPGA designs. Its unique Behavior Extracting Synthesis Technology® (B.E.S.T™) feature performs optimization first at a high level before synthesizing the RTL code into specific FPGA logic. This approach allows for superior optimization across the FPGA, runs fast and supports very large design sizes.

About Actel

Actel is the leader in low power FPGAs and mixed signal FPGAs and offers the most comprehensive portfolio of system and power management solutions. Power Matters. Learn more at ww.actel.com

About Synopsys

Synopsys, Inc. (Nasdaq: SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout

North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com>.

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