

NEWS RELEASE

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Achronix Selects Synopsys as Its Leading EDA Partner

Expanded Agreement Supports Achronix's Product Roadmap

MOUNTAIN VIEW, Calif., June 24, 2009 - Synopsys, Inc., a world leader in software and IP for semiconductor design, verification and manufacturing, today announced that Achronix Semiconductor Corporation, maker of ultra-fast field-programmable gate arrays (FPGAs), has signed an expanded business agreement to establish Synopsys as its leading EDA partner for the design of its next-generation FPGAs. As a result of the new multi-year agreement, Achronix has consolidated on Synopsys' Galaxy(TM) Implementation and Discovery(TM) Verification Platforms throughout their internal development and design flow.

"After an exhaustive evaluation, we selected Synopsys' integrated tool suite for their leading support of advanced processes and their flexible customer support capability," said John Lofton Holt, founder, chairman and CEO, of Achronix. "By leveraging an integrated Synopsys flow, we can more efficiently drive our designs towards advanced process nodes and maintain our path of delivering unprecedented FPGA performance to our customers."

With this expanded agreement, Achronix has chosen products across Synopsys' broad portfolio, including IC Compiler place-and-route technology; Design Compiler® Ultra synthesis; Formality® power-aware equivalence checking; PrimeTime® SI signal integrity analysis ; Star-RCXT(TM) parasitic extraction; Hercules(TM) physical verification; TetraMAX® automatic test pattern generation; the VCS® MX, HSPICE®, and HSIM® simulators for analog and digital verification; and DesignWare® Library's portfolio of synthesizable and verification IP.

"Achronix's focus on delivering leading-edge chips that combine the speed of ASICs with the flexibility of FPGAs is an excellent match to Synopsys' broad technology portfolio," said John Chilton, senior vice president of marketing and strategic development at Synopsys. "We look forward to helping them continue to innovate with unique programmable solutions at breakthrough levels of performance."

About Synopsys

Synopsys, Inc. (Nasdaq:[SNPS](#) - [News](#)) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys' comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, software-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has more than 65 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

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