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### **Synopsys Receives IEEE Standards Association Corporate Award for 2010**

#### ***Recognition for Long-term Standards and Interoperability Commitment and Leadership***

**MOUNTAIN VIEW, Calif. – December 6, 2010** - Synopsys, Inc. (Nasdaq: SNPS), a world leader in software and IP for semiconductor design, verification and manufacturing, today announced it has received the Corporate Award from the IEEE Standards Association (IEEE-SA) for its contribution to semiconductor and Electronic Design Automation (EDA) standards and its role in fostering innovation and collaboration in the standards community. The award is presented annually to an IEEE-SA member who demonstrates outstanding leadership and contributions to the organization.

“Standards are the backbone of interoperable solutions required for successful design and manufacturing of today’s complex semiconductor devices,” said Dr. W. Charlton Adams, Jr., president of the IEEE Standards Association. “As an industry leader and visionary, Synopsys continues to demonstrate how to create and deploy the necessary standards that help the users and providers of these interoperable solutions across the entire ecosystem.”

“Standards bring customers and competitors together in search of common ground. We are honored to be recognized by the IEEE Standards Association for Synopsys’ 20 year commitment to and leadership in standards development and innovation,” said Rich Goldman, vice president of corporate marketing and strategic alliances at Synopsys. “Previous recipients of this award encompass many outstanding technology leaders, and it is a privilege to be included

among them. As pioneers of open standards in EDA, our vision is to advance the entire electronic design industry through community collaboration.”

Synopsys has been a long-time member of IEEE and is active in 14 IEEE standards groups, including IEEE 1800™ SystemVerilog Unified Hardware Design, Specification and Verification Language, IEEE 1801™ Standard for Design and Verification of Low Power Integrated Circuits, and IEEE 1666™ Open SystemC for System-Level Design and Modeling.

### **About Synopsys**

Synopsys, Inc. (Nasdaq:SNPS) is a world leader in electronic design automation (EDA), supplying the global electronics market with the software, intellectual property (IP) and services used in semiconductor design, verification and manufacturing. Synopsys’ comprehensive, integrated portfolio of implementation, verification, IP, manufacturing and field-programmable gate array (FPGA) solutions helps address the key challenges designers and manufacturers face today, such as power and yield management, system-to-silicon verification and time-to-results. These technology-leading solutions help give Synopsys customers a competitive edge in bringing the best products to market quickly while reducing costs and schedule risk. Synopsys is headquartered in Mountain View, California, and has approximately 70 offices located throughout North America, Europe, Japan, Asia and India. Visit Synopsys online at <http://www.synopsys.com/>.

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